

**LISTING OF CLAIMS**

1. (Previously presented) An image sensor, comprising:

an image acquisition portion;

an image processing portion, receiving image information from said image acquisition portion, said image processing portion including a CMOS circuitry with CMOS differential outputs having an output impedance;

an image receiving portion, having at least a pair of transistors and an input impedance, receiving said image information from said CMOS outputs, said image processing portion producing a current mode output and said image receiving portion receiving said current mode output; and

an active impedance matching device having a current source, said active impedance matching device being adapted to match said output impedance of said image processing portion to said input impedance of said image receiving portion by adjusting, with said current source, a bias current through said at least a pair of transistors.

2. (Canceled).

3. (Previously presented) A sensor as in claim 1 wherein said impedance matching device comprises a circuit on said image processing portion.

4. (Previously presented) A sensor as in claim 3 wherein an output circuit of said image processing portion includes a transistor adapted to receive a current bias, wherein a magnitude of the current bias sets the output impedance of said image processing portion.

5. (Canceled).

6. (Previously presented) A sensor as in claim 1 wherein said impedance matching device comprises a circuit on said image receiving portion.

7. (Canceled).

8. (Previously presented) A sensor comprising:

an image acquisition portion;

an image processing portion, receiving image information from said image acquisition portion, said image processing portion including a CMOS circuitry with CMOS differential outputs having an output impedance;

an image receiving portion, having an input impedance, receiving said image information from said CMOS outputs, said image processing portion producing a current mode output and said image receiving portion receiving said current mode output; and

an active impedance matching device, said active impedance matching device being adapted to match said output impedance of said image processing portion to said input impedance of said image receiving portion; and

a current mode driver having an output voltage swing of less than 0.5 volts.

9. (Previously presented) A sensor as in claim 1 wherein said impedance matching portion comprises a first circuit on said image processing portion and a second circuit on said image receiving portion.

10. (Previously Presented) A sensor as in claim 9 wherein said first and second circuits include respective elements adapted to receive respective current biases, and wherein respective magnitudes of the current biases set the respective impedances.

11. (Previously presented) A sensor comprising:

an image acquisition portion;

an image processing portion, said image processing portion being adapted to receive image information from said image acquisition portion at a differential input; and

an impedance matching device, said impedance matching device being adapted to match an output impedance of said image acquisition portion to an input impedance of said image processing portion by adjusting bias current through at least one biased device in a way that renders said input impedance relatively independent of an input current; and

a current mode driver having an output voltage swing of less than 0.5 volts, wherein said image acquisition portion and said image processing portion each operate in current mode.

12. (Previously presented) A sensor as in claim 1, wherein said image receiving portion includes a current mirror part, that mirrors an input current.

13. (Previously presented) A sensor as in claim 1 wherein said image acquisition portion is an active pixel sensor with a photosensor, an in-pixel buffer, and an in pixel select transistor.

14. (Previously presented) An image sensor, comprising:

an image acquisition portion, wherein said image acquisition portion is an active pixel sensor with a photosensor, an in-pixel buffer, and an in pixel select transistor;

an image processing portion, receiving image information from said image acquisition portion, said image processing portion including a CMOS circuitry with CMOS differential outputs having an output impedance;

an image receiving portion, having an input impedance, receiving said image information from said CMOS outputs, said image processing portion producing a current mode output and said image receiving portion receiving said current mode output; and

an active impedance matching device, said active impedance matching device being adapted to match said output impedance of said image processing portion to said input impedance of said image receiving portion;

wherein said image acquisition portion and said image processing portion operates at substantially zero voltage.

15. (Previously Presented) An image sensor, comprising:

an image acquisition portion;

an image processing portion having a current source, said image processing portion being adapted to receive image information from said image acquisition portion at a differential input; and

an impedance matching device having a current source, said impedance matching device being adapted to match an output impedance of said image acquisition portion to an input impedance of said image processing portion by adjusting bias current, from said current source, through said at least a pair of transistors in a way that renders said input impedance relatively independent of an input current.

16. (Original) An image sensor as in claim 15, wherein said image acquisition portion and said image processing portion each operate in current mode.

17. (Previously presented) An image sensor, comprising:

an image acquisition portion;

an image processing portion, said image processing portion being adapted to receive image information from said image acquisition portion at a differential input; and

an impedance matching device, said impedance matching device being adapted to match an output impedance of said image acquisition portion to an input impedance of said image processing portion by adjusting bias current through at least one biased device in a way that renders said input impedance relatively independent of an input current;

wherein said image acquisition portion and said image processing portion each operate in current mode, and

wherein said image acquisition portion and said image processing portion operate at substantially zero voltage.